



2823

Attorney's Docket No.: 10417-088001 / F51-134014M/TOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Koji Iketani et al.
Serial No. : 09/900,093
Filed : July 6, 2001
Title : SEMICONDUCTOR DEVICE MANUFACTURING METHOD
Commissioner for Patents
Washington, D.C. 20231

Art Unit : 2823
Examiner : Toledo, Fernando L.

#8
Response
J. Miller
4/23/03

REPLY

In reply to the action mailed January 13, 2003, please review the remarks presented below. Applicant asks that all claims be allowed in view of the remarks contained on the following sheets, a total of 13 pages.

The initial three-month response due date from the mailing date fell on Sunday, April 13, 2003. Thus, it is submitted that this response filed April 14, 2003 is timely filed and that no fee is required.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

4/14/03

Chris T. Mizumoto
Reg. No. 42,899

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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